**CROSSWORD PUZZLE –I**

Designed by Pratibha Pai

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| **1** | **2** |  |  | **3** |  |  |  |  |  |
|  |  |  |  | **4** |  | **5** |  |  |  |
|  |  |  |  |  |  | **6** |  |  |  |
|  | **7** | **8** |  |  |  |  |  |  |  |
|  |  |  |  | **9** |  | **10** |  | **11** |  |
|  | **12** |  |  |  |  |  |  |  |  |
| **13** |  |  |  |  |  | **14** |  |  |  |
|  |  |  |  |  |  |  |  |  | **15** |
| **16** |  | **17** |  | **18** |  |  |  |  |  |
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|   | Puzzle I MENU |   |   |   |   |   |
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|   | **Across** |  | **Down** |  |  |  |
| 1 | Move accumulator content to register B(5) | 2 | Logically OR accumulator with C register(4) |
| 4 | Decimal adjust addition(3) | 3 | Add immediate 8 bit data with accumulator(3) |
| 6 | Add HL pair content with itself(4) | 5 | Add with carry L register(4) |  |   |
| 7 | Complement carry flag(3) | 8 | Move immediately to accumulator 8 bit data(4) |
| 10 | Load HL pair direct(4) | 9 | Rotate accumulator right(3) |  |   |
| 12 | Enable interrupt(2) | 11 | Load data to accumulator from memory(3) |
| 14 | Copy accumulator content to memory location(3) | 13 | Jump if no carry(3) |  |   |
| 16 | Compare immediately with 8 bit data(3) | 14 | Store from HL pair direct(4) |  |   |
| 18 | Rotate accumulator left with carry(3) | 15 | Unconditional jump(3) |  |   |
|   |   | 17 | Accept data from output port to accumulator(2) |

 Solution at the end

**CROSSWORD PUZZLE –II**

Designed by Pratibha Pai

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| **1** |  |  |  |  |  |  |  |  |  |
| **2** | **3** |  |  | **4**  | **5** |  | **6** |  |  |
|  |  |  |  |  |  |  |  |  |  |
| **7** |  |  |  |  |  |  | **8** |  |  |
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|  |  | **9** |  |  |  | **10** |  |  |  |
|  |  |  |  | **11** |  |  |  | **12** |  |
|  |  | **13** |  |  |  |  |  |  |  |
| **14** |  |  |  |  | **15** | **16** |  |  |  |
|  |  |  |  |  |  |  |  |  |  |
| **17** | **18** |  |  | **19** |  |  |  |  |  |
|  |   |   |   |   |   |  |   |   |   |

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|   | Puzzle II MENU |   |   |   |   |   |
|   |  |  |  |  |  |   |
|   | **Across** |  | **Down** |  |  |  |
| 2 | Rotate accumulator right,9 bit rotation(3) | 1 | Clear accumulator using logical operator(4) |
| 4 | Copy data from B to L register(5) | 3 | Add A content to D content(4) |
| 7 | Add with carry B register to A register(4) | 5 | Copy data from accumulator to output port(3) |  |  |
| 8 | Logically AND immediate data with accumulator(3) | 6 | Same as Across 14(3) |
| 11 | Subtract immediate(3) | 9 | Complement carry flag(3) |  |  |
| 13 | Compare any register content with accumulator(3) | 10 | Enable interrupt(2) |
| 14 | Load data from memory to accumulator(3) | 11 | Set carry flag(in reverse order upwards)(3) |  |  |
| 15 | Move from E register to Accumulator(5) | 12 | Store from accumulator to memory directly(3) |  |  |
| 17 | Copy data to accumulator from output port (2) | 14 | Set HL pair as pointer(4) |  |  |
| 18 | No operation | 16 | Logically OR A with D register(4) |  |  |
| 19 |  Store data from accumulator to address in any pair of registers(4) |  |  |

**PUZZLE –I PUZZLE –II**

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| **1****M** | **2****O** | **V** | **B** | **3****A** |  |  |  |  |  |
|  | **R** |  |  | **4****D** | **A** | **5****A** |  |  |  |
|  | **A** |  |  | **I** |  | **6****D** | **A** | **D** | **H** |
|  | **7****C** | **8****M** | **C** |  |  | **C** |  |  |  |
|  |  | **V** |  | **9****R** |  | **10****L** | **H** | **11****L** | **D** |
|  | **12****E** | **I** |  | **R** |  |  |  | **D** |  |
| **13****J** |  | **A** |  | **C** |  | **14****S** | **T** | **A** |  |
| **N** |  |  |  |  |  | **H** |  |  | **15****J** |
| **16****C** | **P** | **17****I** |  | **18****R** | **A** | **L** |  |  | **M** |
|  |  | **N** |  |  |  | **D** |  |  | **P** |

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| **1****X** |  |  |  |  |  |  |  |  |  |
| **2****R** | **3****A** | **R** |  | **4** **M** | **5****O** | **V** | **6****L** | **B** |  |
| **A** | **D** |  |  |  | **U** |  | **D** |  |  |
| **7****A** | **D** | **C** | **B** |  | **T** |  | **8****A** | **N** | **I** |
|  | **D** |  |  | **C** |  |  |  |  |  |
|  |  | **9****C** |  | **T** |  | **10****E** |  |  |  |
|  |  | **M** |  | **11****S** | **U** | **I** |  | **12****S** |  |
|  |  | **13****C** | **M** | **P** |  |  |  | **T** |  |
| **14****L** | **D** | **A** |  |  | **15****M** | **16****O** | **V** | **A** | **E** |
| **X** |  |  |  |  |  | **R** |  |  |  |
| **17****I** | **18** **N** | **O** | **P** | **19****S** | **T** | **A** | **X** |  |  |
| **H** |   |   |   |   |   | **D** |   |   |   |